

Fig.2

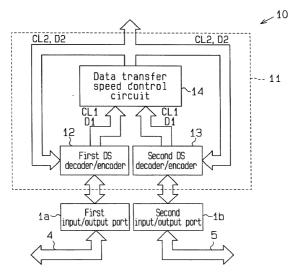


Fig.3

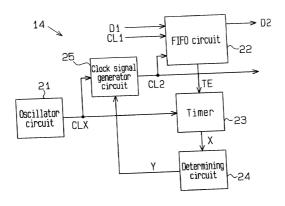


Fig.4

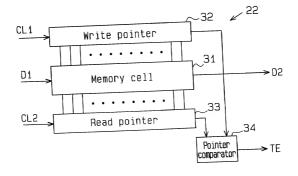


Fig.5

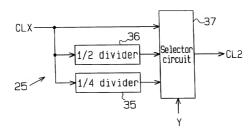


Fig.6

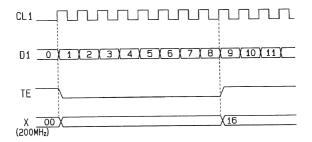


Fig.7 24 χ 41 Z 1 First comparator Comparison circuit value 40 Encoder setting circuit Second Z2 comparator circuit 42

Fig.8

